

J 0 1 1 0
~~292~~ 310

0113 DA 1
7114 5746 02

180 1R7 039 61
CIVUS 0.5 LG
~~3327~~ 3328

PS3089 0112 CR 6
7114 4020

180 1R7 039 61
CIVUS 0.5 LG
~~4914~~ 4892

PS3088 0111 CR5
7114 4025
7158 3009 60
180 1R7 039 61
CIVUS 0.5 LG
~~3869~~ 3908

A diagram of a 4-bit register. Above the register is an input labeled 'R' enclosed in a circle. The register itself is a horizontal bar divided into four segments, each containing a bit of the value '0110'. To the right of the register is the hexadecimal value '&FF'. Below the register are the decimal values '7116' and '5749' followed by '02'.

1. 980mm² 2. 885mm

加工図有り

符号 MARK	日付 DATE	通知NO NOTICE NO	変更内容 CHANGE HEAD	承認 APR CHK	作成 DRA

PRPD	CHKD	APPD
<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
Date 25/4/22	Date 26/4/22	Date 26/4/22

AJ001002: ジョイントテープ巻きは、作業指示書を確認のこと。
AJ001002: Confirm a work instruction for a joint taping.
AJ001001: ジョイントテープ品番の指示書き箇所は、*VTAJ - B W = 45°を巻くこと。
AJ001001: Splice tape number not to be designated, use *VTAJ - B W = 45°

82161- BYC40/BYC20/BYC10/
BYC00/BYQ40



DATE 2021/01/12 TIME 14:26 (7/8)